INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.2444-01	Div. of Appln. No.	09/077;743
Applicant	Hiroshi TOYODA, et al.		
Filing Date	Herewith 6/23/03	Group:	Not Yet Assigned - 2814

U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
Mo	5,470,789	11/1995	Misawa			
1 A	5,578,523	11/1996	Fiordalice et al.			
M	5,877,076	03/1999	Dai			
K	5,880,018	03/1999	Boeck et al.			
(B)	5,985,748	11/1999	Watts et al.			
N	6,433,428	08/2002	Watanabe et al.			

FOREIGN PATENT DOCUMENTS							
Na	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No	
11/10	11-102911	04/1999	Japan			No	
	6-275612	09/1994	Japan			Abstract	
		·					

7	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
A	Wolf, "Silicon Processing for the VLSI Era, Vol. 2 - Process Integration," 1990, Lattice Press, Vol. 2, p. 193

Examiner	1	Date Considered	1/	5/02	1
	ce considered, whether or no if not in conformance and r to applicant.				
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